



NAME: REMYA K P

DESIGNATION: ASST.PROFESSOR

DEPARTMENT: ECE

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ACADEMIC BACKGROUND

1.M.Tech VLSI & Embedded Systems from College of Engineering Chengannur

2.B.Tech Electronics & Instrumentation from College of Engineering Vadamakara

EXPERIENCE

1. Teaching- 6yrs 7 months

2. Industry - Nil

AREA OF INTEREST

VLSI

PROFESSIONAL INFORMATION

PAPER PUBLICATIONS/ PATENT

1. Paper titled “Defects Detection in PCB Using Image Processing And Deep Learning”, International Research Journal of Engineering and Technology (IRJET) Volume 7, Issue 5, May 2020

2. Paper titled “Research of covid 19 with different CT scan Images”, International Research Journal of Engineering and Technology (IRJET) Volume 79, Issue 6, November 2021.

PROFESSIONAL BODY MEMBERSHIP

1. Member of International Association of Engineers. (No: 275356)

CONFERENCES/FDPs ATTENDED (LAST TWO YEARS)

1. Attended a KTU sponsored FDP on **DATA SCIENCE AND ARTIFICIAL INTELLIGENCE** organized by Rajagiri School of Engineering and Technology, Ernakulam on February 2021

2. Attended a KTU sponsored FDP on **Computer vision and Data mining** organized by Rajadhani institute of science and Technology, Palakkad on March 2021

3. Attended a KTU sponsored FDP on **Biomedical instrumentation Research challenges** organized by Amal Jyothi college of Engineering Kanjirappilly on February 2021

4. Attended AICTE- ISTE sponsored FDP on **Machine learning and optimization** from 22-27 February 2021 organized by Ahalia School of engineering and technology, Palakkad.
5. Attended TEQUIP sponsored FDP on **5G technologies and IoT** during March 2021 organized by Government engineering college, Palakkad.
6. Attended APJAKTU Sponsored Five Days Online FDP on "**INDUSTRIAL INSTRUMENTATION AND AUTOMATION**" organized by the Department of EEE ,JCET from 06/09/2021 to 10/09/2021.
7. Attended Microchip Academy Educator Training Program during March 14, 2022 to March 19, 2022 organized by Eduskills

AWARDS/ ACHIEVEMENTS

1. Microchip Academy Educator
2. GATE 2012