



NAME: **SREELAKSHMI S**

DESIGNATION: ASSISTANT PROFESSOR

DEPARTMENT: ECE

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ACADEMIC BACKGROUND

1. M.Tech in Electronics Design Technology
2. B.Tech in Electronics and communication engineering

EXPERIENCE

1. Working as Assistant professor in ECE at Jawaharlal College of engineering and technology from 27.1.2020
2. Worked as Guest Lecturer in ECE at Jawaharlal College of engineering and technology from 23.10.2017 to 14.1. 2020
3. Worked as Graduate Apprendice in ITI limited from 2.12.2013 to 1.12. 2014

AREA OF INTEREST:

ROBOTICS, VLSI

PROFESSIONAL INFORMATION

PAPER PUBLICATIONS/ PATENT

1. Paper titled Implementation of fault tolerant and reconfigurable filters based on error correction codes Sreelakshmi S , Felix M Philip International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE) Volume 6, Issue 5, May 2017

PROFESSIONAL BODY MEMBERSHIP

1. Member of International Association of Engineers. (No: 275355)

CONFERENCES/FDPs ATTENDED (LAST TWO YEARS)

1. Attended AICTE-ISTE Sponsored Online Refresher Programme on Technology Driven Healthcare Systems in January 2022 organized by the Department of Electronics and Communication JCET.
2. Attended APJ KTU Sponsored 5-days Faculty Development Programme on "INDUSTRIAL INSTRUMENTATION AND AUTOMATION" September 2021 organized by the Department of Electrical And Electronics.

3. Attended AICTE- ISTE sponsored FDP on **Machine learning and optimization** from 22-27 February 2021 organized by Ahalia School of engineering and technology, Palakkad.
4. Attended TEQUIP sponsored FDP on **5G technologies and IoT** during March 2021 organized by Government engineering college, Palakkad.
5. Attended a KTU sponsored FDP on **Biomedical instrumentation Research challenges** organized by Amal Jyothi college of Engineering Kanjirappilly on February 2021
6. Attended a KTU sponsored FDP on **Computer vision and Data mining** organized by Rajadhani institute of science and Technology, Palakkad on March 2021
7. Attended a KTU sponsored FDP on **DATA SCIENCE AND ARTIFICIAL INTELLIGENCE** organized by Rajagiri School of Engineering and Technology, Ernakulam on February 2021
8. Attended FDP on **GLOBAL STRATEGIES FOR EDUCATION- POST COVID 19** organised by department of management studies and IQAC cell, JAIN (deemed to be university), Cochin from 23/11/2020 to 30/11/2020.
9. Attended a FDP on “Developing Next Generation Teachers in Higher Education” organized by NGIK from 07/07/2020 to 11/07/2020
10. Attended a FDP on Security in Global Emergency” organized by the Department of Computer Science and Engineering, Nehru College of Engineering and Research Centre, Pambady (Nehru Group of Institutions Kerala).from13/07/2020 to 17/7/2020.
11. Attended FDP on “An induction program for young teachers”organised by Vedavyasa institute of technology. From dec 7 2020 to dec 12 2020.

AWARDS/ ACHIEVEMENTS

FIRST rank in M.Tech Electronics Design Technology